

IN THE UNITED STATES PATENT OFFICE

5

TITLE

LEAD-IN FOR ELECTRONIC BOBBINS

10

INVENTORS

15

John H. Selverian
29 Valley Road
North Reading, MA 01864-1740

20

William D. Koenigsberg
77 High Pine Circle
Concord, MA 01742

25

Joseph L. Parisella
199 East Lothrop Street
Beverly, MA 01915

001. TECHNICAL FIELD

002. This invention relates to electronic devices and more particularly to such devices that are potted with a material to help transfer heat out, to provide protection from moisture, and to attenuate the effects of mechanical shock, etc. One specific, non-limiting example of such a device is a ballast for use with arc discharge lamps.

003. BACKGROUND ART

004. Because of internal thermal dissipation, the temperature of an electronic ballast for arc discharge lamps increases when the ballast is turned on and decreases, that is, returns to ambient temperature, when it is turned off. Repetition of this operation in meeting daily lighting requirements is referred to as temperature cycling. Even when not being used the ballast is subject to the rise and fall of the ambient temperature. This can be critical to the reliability of operation in an outdoor environment, especially in colder climates, because of thermomechanically-induced forces impressed upon circuit board solder joints.

005. Solder joint cracking is often associated with electronic components that are not strain-relieved on ballast circuit boards that are encapsulated in, for example, asphalt-based potting compounds. This occurs because of thermomechanically-induced forces on the circuit board solder joints. This strain is the consequence of two things: temperature cycling of the ballast and the substantial inherent mismatch between the coefficients of expansion of the potting material and the electronic components.

006. Over the temperature range of expected operation, the thermal expansion coefficient of asphalt is nominally 8 to 10 times greater than that of the copper or solder. When the temperature increases, the encapsulating potting material expands, pushing components away from the circuit board; when the temperature decreases, the potting material shrinks, pulling components toward the board. Ultimately, this thermally

induced pushing and pulling fatigues the solder joints, causing mechanical failure and subsequent electrical disconnection within the circuit.

5 007. One of the major components exhibiting these types of problems are bobbins about which wire windings are wrapped, such as transformers and inductors used in the afore-mentioned electronic ballasts. Existing bobbins have lead-ins that serve as rigid connections between the windings and the circuit board. Generally, these lead-ins are either straight or L-shaped.

10 008. It would be an advance in the art if simple modifications could be made to these structures to reduce or eliminate the thermally-induced strain problems caused by thermal cycling.

15 **009. DISCLOSURE OF INVENTION**

0010. It is, therefore, an object of the invention to obviate the disadvantages of the prior art.

0011. It is another object of the invention to enhance strain relief in electronic components.

20 0012. It is yet another object of the invention to achieve these results without extensive modifications of existing bobbins.

0013. These objects are accomplished, in one aspect of the invention, by a coil bobbin that comprises a housing having a floor with at least one electrical lead-in projecting
25 therethrough for attachment to a printed circuit board, the at least one electrical lead-in being provided with thermal-strain relief between the floor and the printed circuit board.

0014. BRIEF DESCRIPTION OF THE DRAWINGS

0015. Fig. 1 is a sectional, elevational view of an embodiment of the invention;

0016. Fig. 2 is a similar view of an alternate embodiment;

5

0017. Fig. 3 is a sectional plan view taken along the line 3-3 of Fig. 2;

0018. Fig. 4 is a graph of temperature-induced strain in a prior art lead-in; and

10 0019. Fig. 5 is a similar graph of temperature-induced strain in a bobbin of the invention.

0020. BEST MODE FOR CARRYING OUT THE INVENTION

0021. For a better understanding of the present invention, together with other and further
15 objects, advantages and capabilities thereof, reference is made to the following
disclosure and appended claims in conjunction with the above-described drawings.

0022. Referring now to the drawings with greater particularity, there is shown in Fig. 1 a
coil bobbin 10 comprising a housing 12 having a floor 14 with at least one electrical
20 lead-in 16 projecting therethrough for attachment to a printed circuit board 18. The at
least one electrical lead-in 16 is provided with thermal-strain relief 20 between the
floor 14 and the printed circuit board 18.

0023. The thermal-strain relief 20 comprises a loop 22 formed in the electrical lead-in 16.
25 The loop can be U-shaped as shown in Fig. 1 or O-shaped as shown in Fig. 3.
Additionally, the loop can be formed parallel to the plane of the circuit board and
floor as shown in Figs. 2 and 3 or it can be normal to that plane, as shown in Fig. 1.
The actual orientation of the loop 20 can be dependent upon the amount of space
existing between the floor 14 and the printed circuit board 18.

0024. The difference in strain relief between the prior art straight and rigid lead-ins and the strain-relieved lead-ins provided by this invention is shown in Figs. 4 and 5. As will be seen in Fig. 4 the peak load induced in the straight lead-ins is about 6 kg while with the strain-relieved lead-in the force drops to about 2.5 kg, a significant difference. This lower load due to the strain relief provided in the lead-in of the present invention results in a significantly longer life of the solder joints and hence of the electronic equipment.

0025. While there have been shown and described what are at present considered to be the preferred embodiments of the invention, it will be apparent to those skilled in the art that various changes and modification can be made herein without departing from the scope of the invention as defined by the appended claims.